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Serial Port Driver SP-block redefinitions

All developers using the variant 4.1 %.J5S03 and %.JWS03 Serial Port Drivers (dated 1/1/94) should be aware of the following corrections to the SP block definition as documented in section 5.1.2 of the "BOS Cobol Assembler Interface Manual V6.1" (MIEMV6.1), section 5.9.1 of the Global Operating Manual V8.0 (BOS on IBM compatibles) (MSM2V8.0) and section 7.9.1 of the Global Operating Manual V8.0 (DOS and Windows) (MSM3V8.0):-

- The 4-byte SPDEV field is redefined as follows:-

```
01      FILLER REDEFINES SPDEV
    02  SPIRQ     PIC  9(4)  COMP    *    INTERRUPT LEVEL
    02  SPPORT    PIC  9(4)  COMP    *    PORT ADDRESS
```

  The SPIRQ field must contain the interrupt level corresponding to the serial port specified in SPPORT (e.g. 4 for COM1, 3 for COM2). THIS VALUE MUST BE NON-ZERO. The potentially dangerous technique of specifying an interrupt level of 0 to allow the Serial Port Driver to dynamically determine the interrupt level is not supported by the variant 4.1 Serial Port Drivers.

  The SPPORT field must contain the port address of the base-address of the serial port (e.g. #03F8 for COM1, #02F8 for COM2). If the SPPORT field contains a value of "UART address + 1" (e.g. #03F9 for COM1, #02F9 for COM2) the Serial Port Driver will attempt to enable the FIFO on an NS-16550A UART. Only enable this option if the selected UART is an NS16550A device (or compatible).

- The 4-byte SPVECT field is unused and should be set to LOW-VALUES.